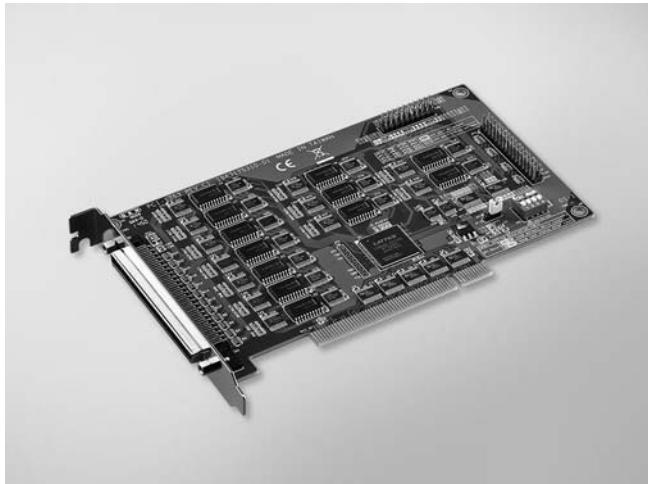


# PCI-1753

96-ch Digital I/O PCI Card



FCC CE RoHS COMPLIANT 2002/95/EC

## Introduction

PCI-1753 is a 96-bit digital I/O card for the PCI bus. The card emulates mode 0 of the 8255 PPI chip, but the buffered circuits offer a higher driving capability than the 8255. The 96 I/O lines are divided into twelve 8-bit I/O ports: A0, B0, C0, A1, B1, C1, A2, B2, C2, A3, B3 and C3. You can configure each port as input or output via software.

## Specifications

### Digital Input/Output

- **Channels** 96 digital I/O lines for PCI-1753
- **Programming Mode** 8255 PPI mode 0
- **Compatibility** 5 V/TTL
- **Input Voltage** Logic 0: 0.8 V max.  
Logic 1: 2.0 V min.
- **Output Voltage** Logic level 0:0.8 V max. @+24mA (Sink)  
Logic level 1:2.0 V min. @-15mA (Source)
- **Interrupt Inputs** 4 (PC00, PC10, PC20, PC30)

### General

- **Bus Type** PCI V2.2
- **I/O Connector** 1 x 100-pin SCSI female connector
- **Dimensions (L x H)** 175 x 100 mm (6.9" x 3.9")
- **Power Consumption** Typical: 5 V @ 400 mA  
Max.: 5 V @ 2.7 A
- **Operating Temperature** 0 ~ 60°C (32 ~ 140°F)
- **Storage Temperature** -20 ~ 70°C (-4 ~ 158°F)
- **Storage Humidity** 5 ~ 95% RH, non-condensing

## Ordering Information

- **PCI-1753** 96-ch Digital I/O PCI Card
- **PCI-1753E** Extension Board for PCI-1753

### Accessories

- **ADAM-3968** 68-pin DIN-rail SCSI Wiring Board
- **ADAM-3968/20** 68-pin SCSI to 20-pin Box Header Board
- **ADAM-3968/50** 68-pin SCSI to 50-pin Box Header Board
- **PCLD-8751** 48-ch Isolated Digital Input Board
- **PCLD-8761** 24-ch Replay/Isolated Digital Input Board
- **PCLD-8762** 48-ch Relay Board
- **PCL-10268-2E** 100-pin to Two 68-pin SCSI Cables, 1 m and 2 m

## Features

- Up to 96 TTL digital I/O lines
- Emulates mode 0 of 8255 PPI
- Buffered circuits for higher driving capability than the 8255
- Multiple-source interrupt handling capability
- Interrupt output pin for simultaneously triggering external devices with the interrupt
- Output status read-back
- "Pattern match" and "Change of state" interrupt functions for critical I/O monitoring
- Keeps the output settings and values after system hot reset
- Supports both dry and wet contact
- High-density 100-pin SCSI connector

## Pin Assignments

PA00	1	51	PA20	PA00 ~PA07: I/O pins of Port A0
PA01	2	52	PA21	PA10 ~PA17: I/O pins of Port A1
PA02	3	53	PA22	PA20 ~PA27: I/O pins of Port A2
PA03	4	54	PA23	PA30 ~PA37: I/O pins of Port A3
PA04	5	55	PA24	PB00 ~PB07: I/O pins of Port B0
PA05	6	56	PA25	PB10 ~PB17: I/O pins of Port B1
PA06	7	57	PA26	PB20 ~PB27: I/O pins of Port B2
PA07	8	58	PA27	PB30 ~PB37: I/O pins of Port B3
PA08	9	59	PA28	PC00 ~PC07: I/O pins of Port C0
PA09	10	60	PA29	PC10 ~PC17: I/O pins of Port C1
PA10	11	61	PA30	PC20 ~PC27: I/O pins of Port C2
PA11	12	62	PA31	PC30 ~PC37: I/O pins of Port C3
PA12	13	63	PA32	GND: Ground
PA13	14	64	PA33	VCC: +5V voltage output
PA14	15	65	PA34	
PA15	16	66	PA35	
PA16	17	67	PA36	
PA17	18	68	PA37	
PA18	19	69	PC22	
PA19	20	70	PC23	
PA20	21	71	PC24	
PA21	22	72	PC25	
PA22	23	73	PC26	
PA23	24	74	PC27	
PA24	25	75	GND	
PA25	26	76	PA30	
PA26	27	77	PA31	
PA27	28	78	PA32	
PA28	29	79	PA33	
PA29	30	80	PA34	
PA30	31	81	PA35	
PA31	32	82	PA36	
PA32	33	83	PA37	
PA33	34	84	PB30	
PA34	35	85	PB31	
PA35	36	86	PB32	
PA36	37	87	PB33	
PA37	38	88	PB34	
PA38	39	89	PB35	
PA39	40	90	PB36	
PA40	41	91	PB37	
PA41	42	92	PC30	
PA42	43	93	PC31	
PA43	44	94	PC32	
PA44	45	95	PC33	
PA45	46	96	PC34	
PA46	47	97	PC35	
PA47	48	98	PC36	
PA48	49	99	PC37	
VCC	50	100	VCC	